

IDEAL POWER INC.

Ideal Power (NASDAQ: IPWR) is pioneering the development of its broadly patented bidirectional power switches, creating highly efficient and eco-friendly energy control solutions for electric vehicle, electric vehicle charging, renewable energy, energy storage, UPS / data center, solid-state circuit breaker and other industrial and military applications. The Company is focused on its patented Bidirectional, Bipolar Junction Transistor (B-TRAN™) semiconductor technology. B-TRAN™ is a unique double-sided bidirectional AC switch able to deliver substantial performance improvements over today's conventional power semiconductors. Ideal Power believes B-TRAN™ modules will reduce conduction and switching losses, complexity of thermal management and operating cost in medium voltage AC power switching and control circuitry. For more information, visit www.IdealPower.com.

CURRENT OPPORTUNITY

Position Title: Director of Device Engineering

Department: Engineering

Location: Austin, TX

POSITION SUMMARY

This is an excellent opportunity for a highly creative power semiconductor engineer/solid state physicist to have a pioneering role in our development and commercialization programs. This role is ideal for a future focused, hands-on engineer who thrives in a fast paced environment and is looking to make a significant technical impact in the power semiconductor industry. The successful candidate will play a pivotal role in the commercialization of a new low-loss, bidirectional power semiconductor architecture. The candidate will play a leading role in product design, optimization, fabrication, testing, manufacturing scale-up, reliability, and long-term technology planning.

ESSENTIAL DUTIES AND RESPONSIBILITIES

- Conduct semiconductor simulations using commonly available tools such as Silvaco or Synopsis TCAD
- Lead device design to incorporate performance improvements identified from simulations and device testing
- Oversee mask layouts and best design practices
- Work closely with third party wafer fabricators to identify, evaluate, and implement process improvements
- Provide overall project management for prototype or low volume contracted semiconductor fabrication development efforts

- Coordinate front end processing of development lots (generation of split plans, trial routes, etc.)
- Perform and/or coordinate electrical characterization of test structures
- Conduct and/or coordinate physical analysis of device and functional test results
- Analyze device data and test structures to optimize device design
- Identify potential performance, manufacturing, testing, and reliability concerns and opportunities for improvement
- Actively participate and contribute to determining technology and product roadmaps and specifying development milestones

CORE SKILLS, EXPERIENCE AND EDUCATION

- Master or PhD degree in Electrical Engineering or solid state physics
- 5 - 10 years of experience in power semiconductor technology development
- Thorough knowledge of power semiconductor topologies with emphasis on MOSFETs and bipolar devices such as IGBTs and BJTs.
- Deep understanding of Si and SiC device physics, fundamental behavior and manufacturing processes
- Demonstrated ability to perform the responsibilities identified above
- Strong conceptual thinking skills, quantitative and qualitative analytical and problem solving skills
- Strong working knowledge of advanced wafer fabrication techniques and processes
- Hands on experience with laboratory equipment and characterization of semiconductors and their applications
- General familiarity with power device packaging, best practices and manufacturability
- General applications knowledge of semiconductor markets such as EV, VFDs, industrial applications (circuit breakers, traction drives), renewable energy, UPS systems a plus
- Good knowledge of basic power conversion topologies, such as, buck, boost and inverter circuit topologies a plus